ABSTRACT OF THE DISCLOSURE

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An initial value register stores gain control amount initial value at the start of a reception frame. At the start of reception frame, a latch unit outputs the value of the gain control amount initial value that has been taken in from initial value register and latched at the end of previous reception frame as a gain control value. A variable gain amplifier amplifies a signal received from mixer in accordance with this initial value. Thereafter, the gain of variable gain amplifier is controlled by a feedback loop structured with an RSSI circuit, a gain control circuit, and a D/A converter circuit, such that the signal level of a reception signal is stabilized at a prescribed level.